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	Filing Date		2005-10-04
	First Named Inventor	Andre	TERECHKO
	Art Unit		2196
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	3	JOAN-MANUEL PARCERISA, ET AL: Efficient Interconnects for Clustered Microsorchiectures, IEEE 2002 (PACT 02)	
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Signature	/Eric M. Bram/	Date (YYYY-MM-DD)	2007-08-14
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